

Notice of References Cited	Application/Control No. 09/915,437	Applicant(s)/Patent Under Reexamination DHONG ET AL	
	Examiner Binh C. Tat	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,442,747	08-2002	Schzukin, Golan	716/18
*	B	US-6,546,541	04-2003	Petranovic et al.	716/18
*	C	US-6,543,036	04-2003	Iyer et al.	716/6
*	D	US-6,360,356	03-2002	Eng, Tommy K.	716/18
*	E	US-6,308,313	10-2001	Lakshminarayana et al.	716/18
*	F	US-2004/0083435	04-2004	Gunderson et al.	716/001
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Yee et al., "Dynamic Logic Synthesis," IEEE. Jan 1997, pp. 345-348
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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